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Inventor Name Search Result

Your Search was:

Last Name = LEE
First Name = JUNG-BAE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08771198	5844438	150	12/20/1996	CIRCUIT FOR GENERATING AN INTERNAL CLOCK FOR DATA OUTPUT BUFFERS IN A SYNCHRONOUS DRAM DEVICES	LEE, JUNG-BAE
08996192	5920511	150	12/22/1997	HIGH- SPEED DATA INPUT CIRCUIT FOR A SYNCHRONOUS MEMORY DEVICE	LEE, JUNG-BAE
08998326	6018259	150	12/24/1997	PHASE LOCKED DELAY CIRCUIT	LEE, JUNG-BAE
09044391	6078546	150	03/18/1998	SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE WITH DOUBLE DATA RATE SCHEME	LEE, JUNG-BAE
09049739	6075384	150	03/27/1998	CURRENT-MODE BIDIRECTIONAL INPUT/OUTPUT BUFFER	LEE, JUNG-BAE
09104152	6130558	150	06/23/1998	DATA TRANSFER CIRCUIT AND METHOD FOR A SEMICONDUCTOR MEMORY	LEE, JUNG-BAE
09136871	6147527	150	08/19/1998	INTERNAL CLOCK GENERATOR	LEE, JUNG-BAE
09168535	6373913	150	10/08/1998	INTERNAL CLOCK SIGNAL GENERATOR INCLUDING CIRCUIT FOR ACCURATELY SYNCHRONIZING INTERNAL CLOCK SIGNAL WITH EXTERNAL CLOCK SIGNAL	LEE, JUNG-BAE
09196994	6232812	150	11/20/1998	INTEGRATED CIRCUIT DELAY LINES HAVING PROGRAMMABLE AND PHASE MATCHING DELAY CHARACTERISTICS	LEE, JUNG-BAE

09235471	6151271	150	01/22/1999	INTEGRATED CIRCUIT MEMORY DEVICES HAVING DATA SELECTION CIRCUITS THEREIN WHICH ARE COMPATIBLE WITH SINGLE AND DUAL RATE MODE OPERATION AND METHODS OF OPERATING SAME	LEE, JUNG-BAE
09318206	6222411	150	05/25/1999	INTEGRATED CIRCUIT DEVICES HAVING SYNCHRONIZED SIGNAL GENERATORS THEREIN	LEE, JUNG-BAE
09356269	6151272	150	07/16/1999	INTEGRATED CIRCUIT MEMORY DEVICES THAT UTILIZE DATA MASKING TECHNIQUES TO FACILITATE TEST MODE ANALYSIS	LEE, JUNG-BAE
09378099	6232797	150	08/20/1999	INTEGRATED CIRCUIT DEVICES HAVING DATA BUFFER CONTROL CIRCUITRY THEREIN THAT ACCOUNTS FOR CLOCK IRREGULARITIES	LEE, JUNG-BAE
09409178	6154416	150	09/30/1999	COLUMN ADDRESS DECODER FOR TWO BIT PREFETCH OF SEMICONDUCTOR MEMORY DEVICE AND DECODING METHOD THEREOF	LEE, JUNG-BAE
09498858	6188631	150	02/07/2000	Semiconductor memory device column select circuit and method for minimizing load to data input/output lines	LEE, JUNG-BAE
09518144	6262938	150	03/03/2000	Synchronous dram having posted cas latency and method for controlling cas latency	LEE, JUNG-BAE
09524037	6240039	150	03/13/2000	Semiconductor memory device and driving signal generator therefor	LEE, JUNG-BAE
09542042	6983010	150	03/31/2000	HIGH FREQUENCY EQUALIZER USING A DEMULTIPLEXING TECHNIQUE AND RELATED SEMICONDUCTOR DEVICE	LEE, JUNG-BAE
09543759	6337809	150	04/05/2000	Semiconductor memory device capable of improving data processing speed and efficiency of a data input and output pin and related method for controlling read	LEE, JUNG-BAE

				and write	
09633240	6678860	150	08/07/2000	INTEGRATED CIRCUIT MEMORY DEVICES HAVING ERROR CHECKING AND CORRECTION CIRCUITS THEREIN AND METHODS OF OPERATING SAME	LEE, JUNG-BAE
09654148	6477107	150	09/01/2000	INTEGRATED CIRCUIT MEMORY DEVICES HAVING DATA SELECTION CIRCUITS THEREIN WHICH ARE COMPATIBLE WITH SINGLE AND DUAL DATA RATE MODE OPERATION AND METHODS OF OPERATING SAME	LEE, JUNG-BAE
09655643	6564287	150	09/05/2000	SEMICONDUCTOR MEMORY DEVICE HAVING A FIXED CAS LATENCY AND / OR BURST LENGTH	LEE, JUNG-BAE
09667379	6272068	150	09/22/2000	Integrated circuit memory devices that utilize data masking techniques to facilitate test mode analysis	LEE, JUNG-BAE
09685266	6414517	150	10/10/2000	INPUT BUFFER CIRCUITS WITH INPUT SIGNAL BOOST CAPABILITY AND METHODS OF OPERATION THEREOF	LEE, JUNG-BAE
09721130	6380799	150	11/22/2000	INTERNAL VOLTAGE GENERATION CIRCUIT HAVING STABLE OPERATING CHARACTERISTICS AT LOW EXTERNAL SUPPLY VOLTAGES	LEE, JUNG-BAE
09826566	6466071	150	04/05/2001	METHODS AND CIRCUITS FOR CORRECTING A DUTY-CYCLE OF A SIGNAL	LEE, JUNG-BAE
09834512	6621371	150	04/13/2001	SYSTEM BOARD AND IMPEDANCE CONTROL METHOD THEREOF	LEE, JUNG-BAE
09875364	Not Issued	71	06/05/2001	Signal transmission circuit and method for equalizing disparate delay times dynamically, and data latch circuit of semiconductor device implementing the same	LEE, JUNG-BAE
10054700	6806582	150	01/17/2002	PAD ARRANGEMENT IN SEMICONDUCTOR MEMORY DEVICE AND METHOD OF	LEE, JUNG-BAE

				DRIVING SEMICONDUCTOR DEVICE	
10081546	6728162	150	02/21/2002	DATA INPUT CIRCUIT AND METHOD FOR SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE	LEE, JUNG-BAE
10154734	6636446	150	05/24/2002	SEMICONDUCTOR MEMORY DEVICE HAVING WRITE LATENCY OPERATION AND METHOD THEREOF	LEE, JUNG-BAE
10172314	6747908	150	06/13/2002	SEMICONDUCTOR MEMORY DEVICE AND METHOD OF SELECTING WORD LINE THEREOF	LEE, JUNG-BAE
10205838	6643201	150	07/26/2002	MEMORY DEVICE HAVING READ CHARGE CONTROL, WRITE CHARGE CONTROL AND FLOATING OR PRECHARGE CIRCITS	LEE, JUNG-BAE
10278071	6819602	150	10/23/2002	MULTIMODE DATA BUFFER AND METHOD FOR CONTROLLING PROPAGATION DELAY TIME	LEE, JUNG-BAE
10281342	6650594	150	10/28/2002	DEVICE AND METHOD FOR SELECTING POWER DOWN EXIT	LEE, JUNG-BAE
10305986	6804163	150	11/29/2002	SEMICONDUCTOR MEMORY DEVICE FOR REDUCING CHIP SIZE	LEE, JUNG-BAE
10453221	6879536	150	06/03/2003	SEMICONDUCTOR MEMORY DEVICE AND SYSTEM OUTPUTTING REFRESH FLAG	LEE, JUNG-BAE
10624783	6826114	150	07/22/2003	DATA PATH RESET CIRCUIT USING CLOCK ENABLE SIGNAL, RESET METHOD, AND SEMICONDUCTOR MEMORY DEVICE INCLUDING THE DATA PATH RESET CIRCUIT AND ADOPTING THE RESET METHOD	LEE, JUNG-BAE
10640146	6826115	150	08/13/2003	CIRCUITS AND METHODS FOR PROVIDING PAGE MODE OPERATION IN SEMICONDUCTOR MEMORY DEVICE HAVING PARTIAL ACTIVATION ARCHITECTURE	LEE, JUNG-BAE

10641637	6965528	150	08/14/2003	MEMORY DEVICE HAVING HIGH BUS EFFICIENCY OF NETWORK, OPERATING METHOD OF THE SAME, AND MEMORY SYSTEM INCLUDING THE SAME	LEE, JUNG-BAE
10716120	Not Issued	95	11/18/2003	ON-DIE TERMINATION CIRCUIT AND METHOD FOR REDUCING ON-CHIP DC CURRENT, AND MEMORY SYSTEM INCLUDING MEMORY DEVICE HAVING THE SAME	LEE, JUNG-BAE
10750093	Not Issued	71	12/31/2003	Memory system mounted directly on board and associated method	LEE, JUNG-BAE
10771488	7016237	150	02/04/2004	DATA INPUT CIRCUIT AND METHOD FOR SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE	LEE, JUNG-BAE
10792425	Not Issued	95	03/03/2004	HIGH BURST RATE WRITE DATA PATHS FOR INTEGRATED CIRCUIT MEMORY DEVICES AND METHODS OF OPERATING SAME	LEE, JUNG-BAE
10797667	Not Issued	41	03/10/2004	Semiconductor memory integrated circuit	LEE, JUNG-BAE
10798469	Not Issued	41	03/11/2004	Semiconductor memory device and method for writing and reading data	LEE, JUNG-BAE
10799783	Not Issued	41	03/12/2004	Internal voltage generating circuit for semiconductor device	LEE, JUNG-BAE
10831702	Not Issued	30	04/23/2004	Memory module and method of testing the same	LEE, JUNG-BAE
10886926	Not Issued	30	07/08/2004	Memory system and timing control method of the same	LEE, JUNG-BAE
10895554	Not Issued	41	07/21/2004	Pad arrangement in semiconductor memory device and method of driving semiconductor device	LEE, JUNG-BAE

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Inventor Name Search Result

Your Search was:

Last Name = KIM
First Name = KYU-HYOUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09521904	6327190	150	03/09/2000	Complementary differential input buffer for a semiconductor memory device	KIM, KYU-HYOUN
09525730	Not Issued	161	03/14/2000	Input circuit having voltage generator shared by multiple input buffers	KIM, KYU-HYOUN
09685266	6414517	150	10/10/2000	INPUT BUFFER CIRCUITS WITH INPUT SIGNAL BOOST CAPABILITY AND METHODS OF OPERATION THEREOF	KIM, KYU-HYOUN
09713968	6366148	150	11/16/2000	Delay locked loop circuit and method for generating internal clock signal	KIM, KYU-HYOUN
09718158	6359481	150	11/22/2000	Data synchronization circuit	KIM, KYU-HYOUN
09808024	6388485	150	03/15/2001	DELAY-LOCKED LOOP CIRCUIT HAVING MASTER-SLAVE STRUCTURE	KIM, KYU-HYOUN
09816968	6452432	150	03/23/2001	SIGNAL PROCESSING CIRCUITS HAVING A PAIR OF DELAY LOCKED LOOP (DLL) CIRCUITS FOR ADJUSTING A DUTY-CYCLE OF A PERIODIC DIGITAL SIGNAL AND METHODS OF OPERATING SAME	KIM, KYU-HYOUN
09826566	6466071	150	04/05/2001	METHODS AND CIRCUITS FOR CORRECTING A DUTY-CYCLE OF A SIGNAL	KIM, KYU-HYOUN
09850019	6459314	150	05/07/2001	DELAY LOCKED LOOP CIRCUIT HAVING DUTY CYCLE CORRECTION FUNCTION AND DELAY LOCKING METHOD	KIM, KYU-HYOUN

09861954	6535051	150	05/21/2001	CHARGE PUMP CIRCUIT	KIM, KYU-HYOUN
09875364	Not Issued	71	06/05/2001	Signal transmission circuit and method for equalizing disparate delay times dynamically, and data latch circuit of semiconductor device implementing the same	KIM, KYU-HYOUN
09935096	6486719	150	08/22/2001	FLIP-FLOP CIRCUITS HAVING DIGITAL-TO-TIME CONVERSION LATCHES THEREIN	KIM, KYU-HYOUN
10101475	6590421	150	03/19/2002	SEMICONDUCTOR DEVICE AND METHOD OF OUTPUTTING DATA THEREIN	KIM, KYU-HYOUN
10108671	6693842	150	03/28/2002	SEMICONDUCTOR DEVICE HAVING A PLURALITY OF OUTPUT SIGNALS	KIM, KYU-HYOUN
10112108	6639868	150	03/28/2002	SDRAM HAVING DATA LATCH CIRCUIT FOR OUTPUTTING INPUT DATA IN SYNCHRONIZATION WITH A PLURALITY OF CONTROL SIGNALS	KIM, KYU-HYOUN
10160703	6704228	150	05/30/2002	SEMICONDUCTOR MEMORY DEVICE POST-REPAIR CIRCUIT AND METHOD	KIM, KYU-HYOUN
10191413	6590434	150	07/10/2002	DELAY TIME CONTROLLING CIRCUIT AND METHOD FOR CONTROLLING DELAY TIME	KIM, KYU-HYOUN
10340831	6734707	150	01/13/2003	DATA INPUT CIRCUIT FOR REDUCING LOADING DIFFERENCE BETWEEN FETCH SIGNAL AND MULTIPLE DATA IN SEMICONDUCTOR DEVICE	KIM, KYU-HYOUN
10405484	6853317	150	04/03/2003	CIRCUIT AND METHOD FOR GENERATING MODE REGISTER SET CODE	KIM, KYU-HYOUN
10611255	6847559	150	07/01/2003	INPUT BUFFER CIRCUIT OF A SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE	KIM, KYU-HYOUN
10619821	Not Issued	30	07/14/2003	Delay locked loop circuit for internally correcting duty cycle and duty cycle correction method	KIM, KYU-HYOUN

				thereof	
10631412	Not Issued	95	07/30/2003	OUTPUT DRIVER CAPABLE OF CONTROLLING SLEW RATE OF OUTPUT SIGNAL ACCORDING TO OPERATING FREQUENCY INFORMATION OR CAS LATENCY INFORMATION	KIM, KYU-HYOUN
10631414	6870776	150	07/30/2003	DATA OUTPUT CIRCUIT IN COMBINED SDR/DDR SEMICONDUCTOR MEMORY DEVICE	KIM, KYU-HYOUN
10645018	6954094	150	08/21/2003	SEMICONDUCTOR MEMORY DEVICE HAVING PARTIALLY CONTROLLED DELAY LOCKED LOOP	KIM, KYU-HYOUN
10656303	6934215	150	09/04/2003	SEMICONDUCTOR MEMORY DEVICE HAVING DUTY CYCLE CORRECTION CIRCUIT AND INTERPOLATION CIRCUIT INTERPOLATING CLOCK SIGNAL IN THE SEMICONDUCTOR MEMORY DEVICE	KIM, KYU-HYOUN
10671105	6980036	150	09/25/2003	SEMICONDUCTOR DEVICE COMPRISING FREQUENCY MULTIPLIER OF EXTERNAL CLOCK AND OUTPUT BUFFER OF TEST DATA AND SEMICONDUCTOR TEST METHOD	KIM, KYU-HYOUN
10672461	6999375	150	09/26/2003	SYNCHRONOUS SEMICONDUCTOR DEVICE AND METHOD OF PREVENTING COUPLING BETWEEN DATA BUSES	KIM, KYU-HYOUN
10716146	Not Issued	41	11/18/2003	Time delay compensation circuit comprising delay cells having various unit time delays	KIM, KYU-HYOUN
10774933	Not Issued	93	02/09/2004	DELAY-LOCKED LOOP (DLL) CAPABLE OF DIRECTLY RECEIVING EXTERNAL CLOCK SIGNALS	KIM, KYU-HYOUN
10793001	7015739	150	03/04/2004	INTEGRATED CIRCUIT DEVICES HAVING DUTY CYCLE CORRECTION CIRCUITS THAT RECEIVE	KIM, KYU-HYOUN

				CONTROL SIGNALS OVER FIRST AND SECOND SEPARATE PATHS AND METHODS OF OPERATING THE SAME	
10793209	Not Issued	95	03/04/2004	DOUBLE DATA RATE SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY SEMICONDUCTOR DEVICE	KIM, KYU-HYOUN
10799783	Not Issued	41	03/12/2004	Internal voltage generating circuit for semiconductor device	KIM, KYU-HYOUN
10837391	Not Issued	30	04/29/2004	Spread spectrum clock generator	KIM, KYU-HYOUN
10841866	Not Issued	71	05/06/2004	Hyper-ring oscillator	KIM, KYU-HYOUN
10884723	Not Issued	93	07/02/2004	BUFFER CIRCUIT AND MEMORY SYSTEM FOR SELECTIVELY OUTPUTTING DATA STROBE SIGNAL ACCORDING TO NUMBER OF DATA BITS	KIM, KYU-HYOUN
10890493	Not Issued	41	07/13/2004	Interface circuit and signal clamping circuit using level-down shifter	KIM, KYU-HYOUN
10925522	Not Issued	71	08/25/2004	Jitter suppressing delay locked loop circuits and related methods	KIM, KYU-HYOUN
10949165	Not Issued	93	09/24/2004	INPUT BUFFER CAPABLE OF REDUCING INPUT CAPACITANCE SEEN BY INPUT SIGNAL	KIM, KYU-HYOUN
10990412	Not Issued	30	11/18/2004	Input buffer for detecting an input signal	KIM, KYU-HYOUN
11005821	Not Issued	41	12/07/2004	Duty cycle correction circuits suitable for use in delay-locked loops and methods of correcting duty cycles of periodic signals	KIM, KYU-HYOUN
11154725	Not Issued	30	06/15/2005	Level shifting circuit and method reducing leakage current	KIM, KYU-HYOUN
11158013	Not Issued	30	06/21/2005	Delay locked loops and methods using ring oscillators	KIM, KYU-HYOUN
11176396	Not Issued	30	07/08/2005	Output driver and method thereof	KIM, KYU-HYOUN
11220180	Not Issued	30	09/06/2005	Sense amplifier with low common mode differential input signal	KIM, KYU-HYOUN
11243369	Not	20	10/04/2005	Semiconductor driver circuit with	KIM, KYU-

	Issued			signal swing balance and enhanced testing	HYOUN
11258565	Not Issued	20	10/25/2005	Semiconductor memory device	KIM, KYU-HYOUN
11325343	Not Issued	20	01/05/2006	Input circuit for a memory device, and a memory device and memory system employing the input circuit	KIM, KYU-HYOUN
11339120	Not Issued	20	01/23/2006	Output driver capable of controlling slew rate of output signal according to operating frequency information or CAS latency information	KIM, KYU-HYOUN
11247846	Not Issued	30	10/11/2005	Impedance adjustment circuits and methods using replicas of variable impedance circuits	KIM, KYU-HYOUNG

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